

Fig. 3B

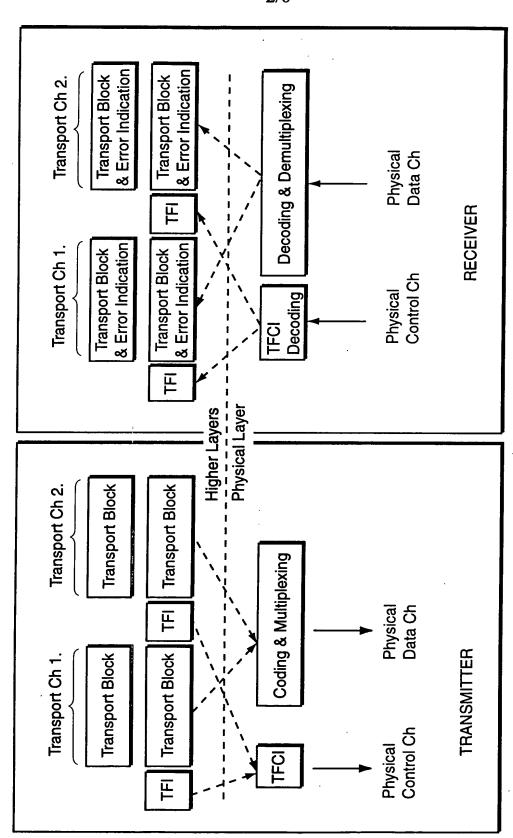
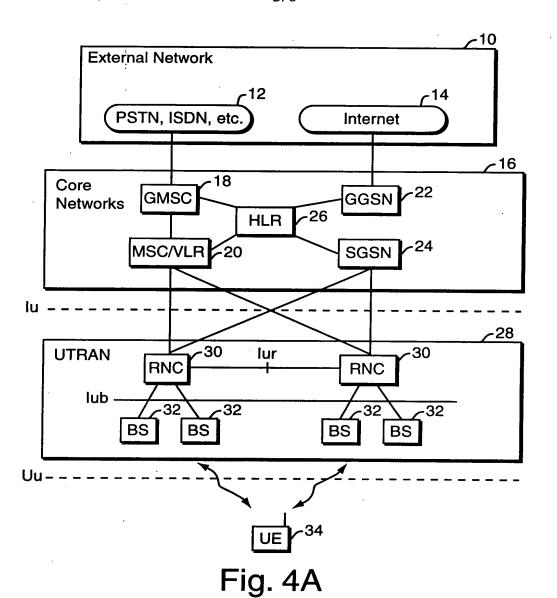


Fig. 2A

Fig. 2B



Radio Unit

A2

Data Processing
Circuitry

Memory

A4

Memory

A4

Fig. 4B

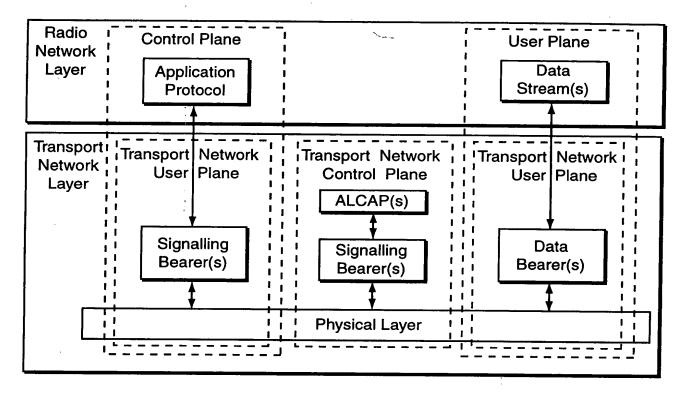


Fig. 5

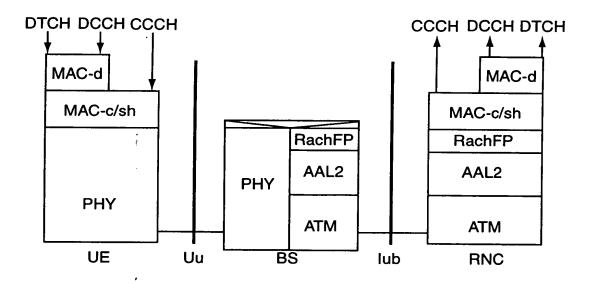
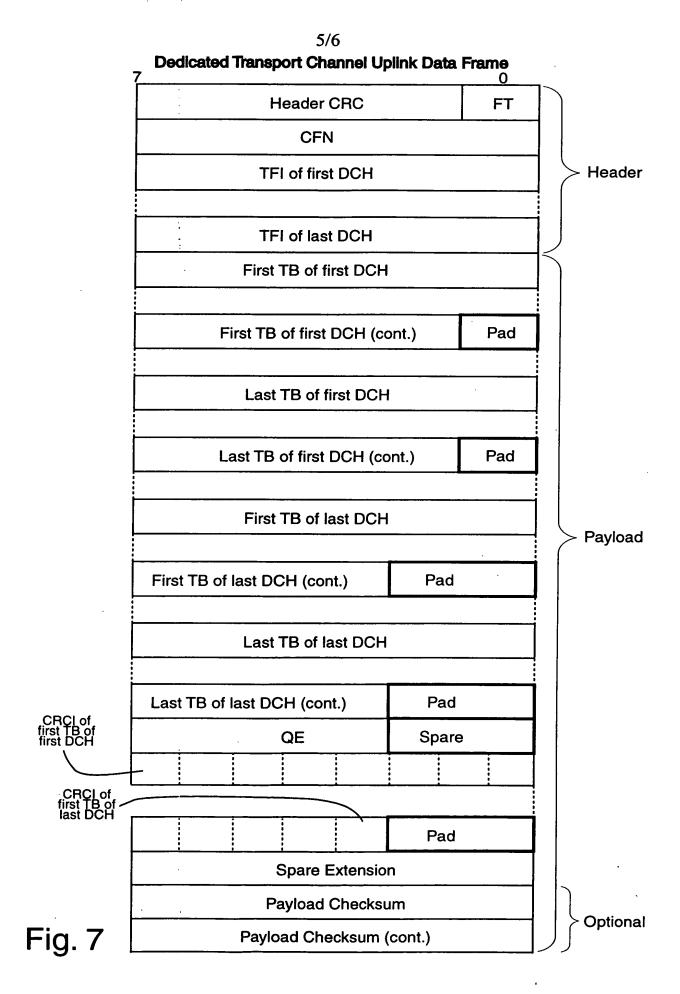


Fig. 6



## **RACH Data Frame Header**

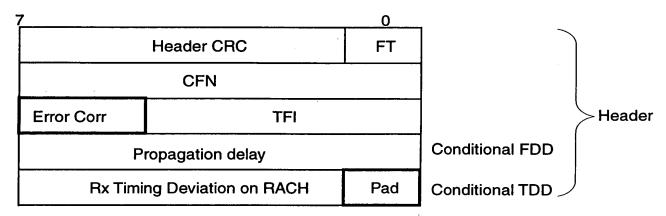


Fig. 8

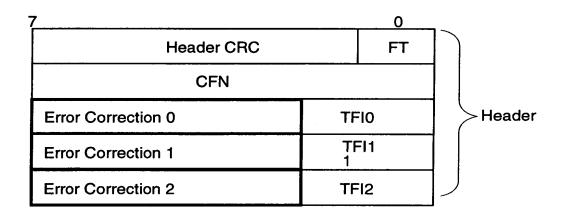


Fig. 9